

EAST - [400.wsp:1]

FileViewEditToolsWindowHelp

Drafts

Pending

Active

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UDC

Queue

Trash

L1: (14) ("6009511" or ("5126963" or ("5748516" or ("5995991" or ("5347841" or ("5347842"...

L2: (6) ("5347481" or ("5347482" or ("6131106")).PN.

L3: (370019) exact or inexact

L4: (3072) (input\$1 or operand\$1) near5 3

L5: (10343) (output\$1 or result\$1) near5 3

L6: (1008) (flag or status) near10 3

L7: (659) 4 with 5

L8: (4) 6 same 7

L9: (22853) 708/.ccls.

L10: (47) 7 and 9

L11: (44) 10 not 8

L14: (10714) 712/.ccls.

L15: (11) 7 and 14 not 10

L16: (44) 6 and 9

L17: (38) 16 not (10 or 15)

L18: (25) 6 and 14 not (10 15 17)

Search

USPAT:US:POPUB:EPO:JPO:IPurele

Default operator: OR

Highlight all hit terms initially

BRD Item

SAF Item

Image

Text

HTML

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cl	Inventor	S	C	P			
1	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6378067 B1	20020423	13	Exception reporting architecture for SIMD-FP instructions	712/244			Golliver, Roger et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
2	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 5421021 A	19950530	17	Circuit including a latching unit for holding instructions during ex	712/211	712/244		Saini, Avtar K.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
3	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6099158 A	20000808	21	Apparatus and methods for execution of computer instructio	708/505	708/497; 708/499;		Gorshtein, Valery Y. et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
4	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6594681 B1	20030715	19	Quotient digit selection logic for floating point division/square roo	708/504	708/653		Prabhu, J. Arjun	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
5	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 5963461 A	19991005	22	Multiplication apparatus and methods which generate a shift	708/503	708/205; 708/209;		Gorshtein, Valery Y. et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
6	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 5812439 A	19980922	13	Technique of incorporating floating point information into pr	708/497	712/222		Hansen, Craig C.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
7	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 5892697 A	19990406	16	Method and apparatus for	708/496	708/204;		Brakefield, James	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>

Ready

NUM